

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,948,098 B2
APPLICATION NO. : 09/822052
DATED : September 20, 2005
INVENTOR(S) : Sanjay Ramakrishna Pillay and Raghunath Kirisha Rao

Page 1 of 3

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 19, Line 67, last paragraph, "he" should be -- the --;

Column 20, Line 2, "706k" should be --706 --;

Column 25, Line 42, "disturbe" should be -- disturb --;

Column 25, Line 46, "688" should be -- 68 --;

Column 27, Line 2, "FIFO_1 STATE" should be -- FIFO_1_STATE --;

Column 27, last line, "microprocessor" should be -- Microprocessor --;

Column 40, Line 17, "STC_COUNTERO" should be -- STC_COUNTER0 --;

Column 42, last line, after "weak1", the following sentence should be inserted:
-- Tables 173-175 illustrate the mapping for the microprocessor 101
memory access projection registers. --;

Column 48, Table 1, description for LineLength, line beginning "dots = 1", "andid"
should be -- andld --;

Column 57, Table 26, Line 6 of header, "master" should be -- masters --;

Column 57, Table 27, Line 4 of header, "master" should be -- masters --;

Column 58, Table 28, Line 5 of header, "master" should be -- masters --;

Column 60, Table 34, Type for "SDEC" should be -- R/W --;

Column 64, Table 49, Lines 30, 33, 36 and 39, "harware" should be -- hardware --;

Column 75, Table 65, Line 24, should read -- prevents engine from starting if it has --;

Column 88, Table 95, Line 1 of table, "d_Manufacturer" should be
-- d_iManufacturer --;

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Column 95, Table 111, First line of Header should read
-- EP2DESC1 (Endpoint 2 Descriptor1, --;

Column 97, Table 113, the Reset Value for "ep3_wMaxPacket Size" should be
-- 0x40 --;

Column 102, Table 130, Line 61, "s_pmdk" should be -- sp_mdk --;

Column 109, Table 146, Line 1 of header should begin with -- GPIO Interrupt Mask
Register --;

Column 109, Table 147, next to last line of table, "generated" should be -- generates --;

Column 111, Table 148, Line 12, "GPIO[2]" should read -- GPIO[12] --;

Column 116, Table 157, second line of table "sae" should be -- case --;

Column 125, Table 177, Function Block for line beginning "8008_0000 - 8008_FFFF"
should read -- Remap Pause Control --;

Column 129, Table 179, Function Block for line beginning "8003 0000 - 8003 FFFF"
should read -- Battery Volume Checker --;

Column 129, Table 180, second line of header, "Defirdtlon" should be -- Definition --;

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Spanning Columns 131 & 132, Table 188, the description for DiePad 12 should read
-- DCL2/GPIO1 pin <12> default) --;

Spanning Columns 131 & 132, Table 188, the Signal Name for 17 (second occurrence)
should be -- _VDD_CORE --.

Signed and Sealed this

Seventh Day of October, 2008

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS
Director of the United States Patent and Trademark Office